

DOCKET NO: S1022.80655US00

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Andrew C. STURGES

Serial No:

09/842,312

Confirmation No:

6679

Filed:

April 25, 2001

For:

COMPUTER SYSTEM AND METHOD FOR FETCHING,

DECODING, AND EXECUTING INSTRUCTIONS

Examiner:

Richard L. Ellis

Art Unit:

2183

## **CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to MAIL STOP Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the 400 day of June, 2004.

Janine Michalski Signature

## MAIL STOP Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

# STATEMENT FILED PURSUANT TO THE DUTY OF DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

## PART I: Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement has been filed more than three months after the filing date of this application and after the mailing date of the first Office Action, but before the mailing date of either a final action under 37 C.F.R. §1.113 or a Notice of Allowance under 37 C.F.R. §1.311, or an action that otherwise closes prosecution in this application.

The fee of \$180.00 as set forth in 37 C.F.R. §1.17(p) is enclosed.

795167.1 06/09/2004 MAHMED1 00000006 09842312 01 FC:1806 180.00 OP

# PART II: Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

#### PART III: Remarks

Documents cited anywhere in the Information Disclosure Statement are enclosed unless otherwise indicated. It is respectfully requested that:

- 1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
- 2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
- 3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

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Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted, Andrew C. STURGES, Applicant

Bv:

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Docket No. S1022.80655US00

Date: June 4, 2004

X06/04/04X

FORM PTO-1449/A and B (ModRed)

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

APPLICANT:

APPLICANT:

April 25, 2001

CONFIRMATION NO.: 6679

APPLICANT:

Andrew C. STURGES

GROUP ART UNIT: 2183

EXAMINER: Richard L. Ellis

#### U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited	Date of Publication or of issue	
		Number	Kind Code	Document	of Cited Document MM-DD-YYYY	
	A1*	3,426,330		H.B. Marx, et al.	01-02-1969	
	A2*	3,573,854		Watson, et al.	04-06-1971	
	A3*	4,974,155		Dulong, et al.	11-27-1990	
	A4*	5,615,386		Amerson, et al.	03-25-1997	

#### FOREIGN PATENT DOCUMENTS

Examiner's	Cite	Foreign Patent Document			Name of Patentee or Applicant of Cited	Date of Publication of	Translation
Initials	No.	Office/ Country	Number	Kind Code	Document (not necessary)	Cited Document MM-DD-YYYY	(Y/N)
	B1*	EP	A-0 355 069		Evans & Sutherland Computer Corp.	02-21-1990	
	B2*	EP	A-0 219 203		Case et al.	02-15-1995	

#### OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Cite Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.  C1* European Search Report from United Kingdom Patent Application Number 9412487.2, filed June 22, 1994.  C2* International Journal Of Mini And Microcomputers, vol. 11, no. 1, 1989, Calgary, California US, pp 13-17.  Cortadella and Llaberia "Making Branches Transparent To the Execution Unit".  C3* Proceedings 4th MIT Conference: Advanced Research In VLSI, April 7, 1986, Cambridge, MA, US pp 73-88, Plaszkun and Farrens "An Instruction Cache Designs For Use With a Delayed Branch".	Translat (Y/N	
publisher, city and/or country where published.  C1* European Search Report from United Kingdom Patent Application Number 9412487.2, filed June 22, 1994.  C2* International Journal Of Mini And Microcomputers, vol. 11, no. 1, 1989, Calgary, California US, pp 13-17.  Cortadella and Llaberia "Making Branches Transparent To the Execution Unit".  C3* Proceedings 4th MIT Conference: Advanced Research In VLSI, April 7, 1986, Cambridge, MA, US pp 73-88,	(Y/N	
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C1* European Search Report from United Kingdom Patent Application Number 9412487.2, filed June 22, 1994.  C2* International Journal Of Mini And Microcomputers, vol. 11, no. 1, 1989, Calgary, California US, pp 13-17. Cortadella and Llaberia "Making Branches Transparent To the Execution Unit".  C3* Proceedings 4th MIT Conference: Advanced Research In VLSI, April 7, 1986, Cambridge, MA, US pp 73-88,		
Cortadella and Llaberia "Making Branches Transparent To the Execution Unit".  C3* Proceedings 4th MIT Conference: Advanced Research In VLSI, April 7, 1986, Cambridge, MA, US pp 73-88,		
This Examination of the Mistration Cathologist of Coo With a Bolayea Station .		
C4* IBM Technical Disclosure Bulletin, vol. 14, no. 12, May 1972, New York, US, pp 3599-3611, Beebe et al, "Instruction Sequencing Control".		
C5* Computer Architecture: A Quantitative Approach, David A. Patterson, et al., pages 265-270 (Chapter 6, Sectopm 4).	IVE	D.
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EXAMINER	DATE CONSIDERED	Technology Center 2100
•		resimology center 2150

#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[NOTE - Must provide a copy of any patent, publication, other information listed, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]

<sup>\*</sup>a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. <u>09/356,122</u>, filed <u>July 16, 1999</u>, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).